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<b>TRANSMITTAL FORM</b> <small>(to be used for all correspondence after initial filing)</small>	Application / Conf. No.	10/783,589 /
	Filing Date	February 20, 2004
	First Named Inventor	Kevin T. Look
	Examiner Name	Unknown
	Art Unit	Unknown
Mail Stop:	Patent No.	
Express Mail Receipt No.	Attorney Docket Number	X-1462-2P US
Total Number of Pages in This Submission		

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm or Customer Number	24309	Reg. Number 51,959
Attn: Justin Liu	(Customer Number)	
Signature		
Date	April 16, 2004	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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Signature		Date	April 16, 2004

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X-1462-2P US

PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Kevin T. Look et al.  
Assignee: Xilinx, Inc.  
Title: "Regulating Unused/Inactive Resources in Programmable Logic Devices for Static Power Reduction"  
Serial No.: 10/783,589 File Date: February 20, 2004  
Examiner: Unknown Art Unit: Unknown  
Docket No.: X-1462-2P US

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the three (3) references listed in the attached Substitute for Form PTO-1449A/PT. A copy of each is enclosed herein.

These references were cited in related U.S. patent applications. This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,

Justin Liu  
Attorney for Applicants  
Reg. No. 51,959

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Julie Matthews  
Name

  
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Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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**Complete if Known**

Application / Conf. No.

10/783,589

Filing Date
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February 20, 2004

First Named Inventor

Kevin T. Look

Art Unit

Unknown

Examiner Name

Unknown

Attorney Docket Number

X-1462-2P IIS

## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

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Examiner  
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Date \_\_\_\_\_

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09/02/2008

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<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language translation is attached.

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Application / Conf. No.

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1

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February 20, 2004

First Named Inventor

Kevin T. Look

Art Unit

Unknown

Examiner Name

Unknown

Attorney Docket Number

X-1462-2P IIS

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T 3

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TAKAHASHI, M. et al.; "A 60-mW MPEG4 Video Codec Using Clustered Voltage Scaling with Variable Supply-Voltage Scheme," November 1998, pages 1772-1780, Vol. 33, No. 11, available from IEEE Journal of Solid-State Circuits, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997.

Examiner  
Signature

/Daniel Chang/

Date Considered

09/02/2008

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